



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yuichi HASHIMOTO et al.

Serial No.: 10/714,931

Filed: November 18, 2003 Attn: BOX MISSING PARTS
For: METHOD AND CIRCUIT FOR ERROR CORRECTION, ERROR
CORRECTION ENCODING, DATA REPRODUCTION,
OR DATA RECORDING

TRANSLATOR'S DECLARATION

Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

Sir:

I, Junichi KAWABATA, declare:

that I am thoroughly familiar with both the Japanese and English languages;

that the attached document represents an accurate English translation of U.S. Patent Application Serial No. 10/714,931 filed November 18, 2003; and

That I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: June 1, 2004


Junichi KAWABATA